

#7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Richard SPITZ  
International Application No. : PCT/DE00/02918  
International Filing Date : August 26, 2000  
U.S. Serial No. : 10/070,653  
For : **SEMICONDUCTOR ELEMENT AND METHOD  
OF MANUFACTURING THE SEMICONDUCTOR  
COMPONENT**

Assistant Commissioner  
for Patents  
Box PCT  
Washington, D.C. 20231

Attention: DO/EO/US

**RESPONSE TO MISSING REQUIREMENTS  
UNDER 35 U.S.C. 371**

S I R :

In response to the Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office (DO/EO/US) (mailed July 20, 2002), Applicant submits herewith a fully executed Declaration, in order to complete the filing requirements for the U.S. national phase of the above-identified PCT application. The application filed in the Patent Office is the application which the inventors executed by signing the Declaration and Power of Attorney. A copy of the Notification of Missing Requirements is also enclosed.

Please note accompanying this response is an Information Disclosure Statement.

The Office is authorized to charge the \$130.00 fee to cover the surcharge for late filing of the Declaration and any additional fees to Deposit Account No. **11-0600**.

07/26/2002 LLANDRA 00000038 110600 10070653  
01 FC:154 130.00 CH

Date:

7/22/02

Respectfully submitted,

KENYON & KENYON

By: *[Signature]*  
*[Signature]*

Richard L. Mayer, Reg. No. 22,490  
One Broadway  
New York, NY 10004  
Tel: (212) 425-7200  
Fax: (212) 425-5288